

CLAIMS

1. A method of transmitting a serial instruction frame into an integrated circuit to disable a test port circuit comprising the steps of:

(a) providing within said integrated circuit a test port linking circuit having a register for receiving a first portion of said serial instruction frame;

(b) providing within said integrated circuit a test port circuit having a register for receiving a second portion of said serial instruction frame;

(c) transmitting the serial instruction frame to said registers of said test port linking and test port circuits; and

(d) responding to the instruction frame portion transmitted into the register of the test port linking circuit to disable further transmission of instruction frames into said test port circuit.

2. A method of transmitting a serial instruction frame into an integrated circuit to enable a test port circuit comprising the steps of:

(a) providing within said integrated circuit a test port linking circuit having a register for receiving said serial instruction frame;

(b) providing within said integrated circuit a test port circuit;

(c) transmitting said serial instruction frame to said register of said test port linking circuit; and

(d) responding to the instruction frame transmitted into the register of the test port linking circuit to enable transmission of further instruction frames into said test port circuit.